





D2-0814

Docket No.: M4065.0741/P741  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Kit Fai Chan, et al.

Application No.: N/A

Group Art Unit: N/A

Filed: December 12, 2003

Examiner: Not Yet Assigned

For: CURRENT MODE LOGIC SCHEME AND  
CIRCUIT FOR MATCH LINE SENSE  
AMPLIFIER DESIGN USING CONSTANT  
CURRENT BIAS CASCODE CURRENT  
MIRRORS

POWER OF ATTORNEY BY ASSIGNEE AND  
CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(B)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Micron Technology, Inc., Assignee of the entire right, title and interest in the above-identified application by virtue of the true copy of the Assignment attached hereto, hereby appoints the attorneys and agents of the firm of Dickstein Shapiro Morin & Oshinsky LLP located at 2101 L Street NW, Washington, DC 20037-1526, listed as follows:

All practitioners at Customer Number 24998.

and also, listed as follows:

Charles B. Brantley, III, 38,086; Michael L. Lynch, 30,871; Kevin D. Martin, 37,882; David J. Paul, 34,692; and Russ Slifer, 39,838.

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attorneys of Micron Technology, Inc. as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.


The Assignee certifies that the above-identified assignment has been reviewed and to the best of the Assignee's knowledge and belief, title is in the assignee.

Please direct all correspondence regarding this application to the following:

Thomas J. D'Amico  
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Washington, DC 20037-1526  
Telephone: (202) 785-9700  
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Respectfully submitted,

MICRON TECHNOLOGY, INC.

  
\_\_\_\_\_  
Michael L. Lynch  
Chief Patent Counsel  
Registration No. 30,871

Dated: 3-17-2004

## ASSIGNMENT AND AGREEMENT

For value received, we, Kevin Chan and Brent Haukness, hereby sell, assign and transfer to Micron Technology, Inc., a corporation of the State of Delaware, having an office at 8000 S. Federal Way, Boise, Idaho 83706-9632, U.S.A., and its successors, assigns and legal representatives, the entire right, title and interest, for the United States of America, in and to certain inventions related to an invention entitled CURRENT MODE LOGIC SCHEME AND CIRCUIT FOR MATCH LINE SENSE AMPLIFIER DESIGN USING CONSTANT CURRENT BIAS CASCODE CURRENT MIRRORS, described in an application for Letters Patent of the United States, executed by us of even date herewith, and all the rights and privileges in said application and under any and all Letters Patent that may be granted in the United States for said inventions; and we also concurrently hereby sell, assign and transfer to Micron Technology, Inc. the entire right, title and interest in and to said inventions for all countries foreign to the United States, including all rights of priority arising from the application aforesaid, and all the rights and privileges under any and all forms of protection, including Letters Patent, that may be granted in said countries foreign to the United States for said inventions.

We authorize Micron Technology, Inc. to make application for such protection in its own name and maintain such protection in any and all countries foreign to the United States, and to invoke and claim for any application for patent or other form of protection for said inventions, without further authorization from us, any and all benefits, including the right of priority provided by any and all treaties, conventions, or agreements.

We hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any document which may be required in any country in proof of the right of Micron Technology, Inc. to apply for patent or other form of protection for said inventions and to claim the aforesaid benefit of the right of priority.

We request that any and all patents for said inventions be issued to Micron Technology, Inc. in the United States and in all countries foreign to the United States, or to such nominees as Micron Technology, Inc. may designate.

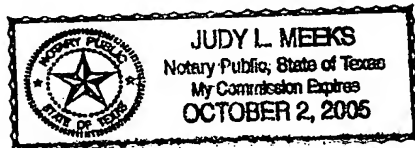
We agree that, when requested, we shall, without charge to Micron Technology, Inc. but at its expense, sign all papers, and do all acts which may be necessary, desirable or convenient in connection with said applications, patents, or other forms of protection.

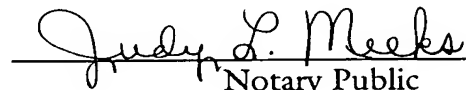
  
\_\_\_\_\_  
Kit Fai Chan

Date: 12/3/2003

United States of America )  
State of Texas ) ss.:  
County of Brazos )

On this 3rd day of December, 2003, before me personally came Kit Fai Chan, to me known to be the individual described in and who executed the foregoing instrument, and acknowledged execution of the same.



  
\_\_\_\_\_  
Notary Public

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ASSIGNMENT AND AGREEMENT

Signature Page for Second Inventor

Brent Haukness  
Brent Haukness

Date: 9.4.03.

United States of America )  
State of CA. ) ss.:  
County of Santa Clara. )

On this 4 day of September., 2003, before me  
personally came Brent Haukness, to me known to be the individual  
described in and who executed the foregoing instrument, and acknowledged execution  
of the same.

Dina M. Borromeo  
Notary Public

